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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/681,509	10/07/2003	2814	2514	SAND-01135US0	5	62	11

CONFIRMATION NO. 9643

CORRECTED FILING RECEIPT

64948
VIERRA MAGEN/SANDISK CORPORATION
575 MARKET STREET
SUITE 2500
SAN FRANCISCO, CA94105

Date Mailed: 02/14/2007

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Applicant(s)

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Assignment For Published Patent Application

Matrix Semiconductor, Inc.

Power of Attorney: The patent practitioners associated with Customer Number 64948

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 12/31/2003

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/681,509**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

METHOD OF UNIFORM SEEDING TO CONTROL GRAIN AND DEFECT DENSITY OF
CRYSTALLIZED SILICON FOR USE IN SUB-MICRON THIN FILM TRANSISTORS

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